

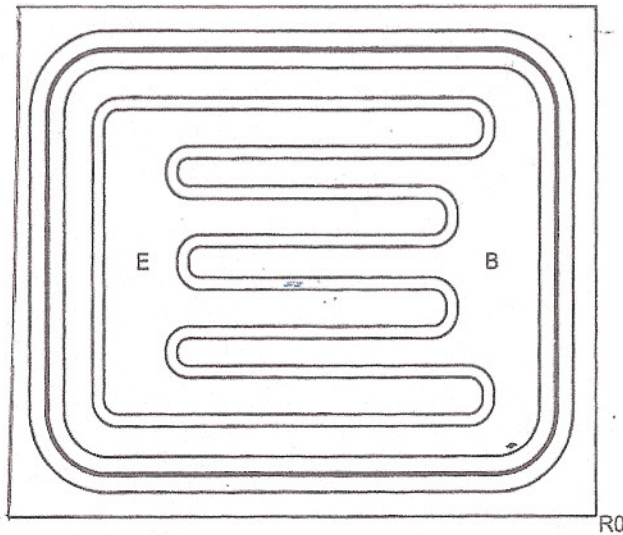


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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



BACKSIDE COLLECTOR

Top Material: Al
Backside Material: Cr/Ni/Ag
Bond Pad Size: 12 x 32 (base) 13 x 46 (emitter)
Backside Potential: Collector
Mask Ref: CP611

APPROVED BY: MG

DIE SIZE : 80 x 99 Mils

DATE: 4/18/11

MFG: Central Semi

THICKNESS: 12.5 Mils

P/N: 2N6107